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2100 #3  
amended  
MMA.  
3/11/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named  
Inventor : Partha P. Datta Ray et al.

2123

Serial No.: 09/964,030

Filed : September 26, 2001

Group Art Unit:

For : METHOD AND APPARATUS FOR  
ADAPTIVE TIMING OPTIMIZATION  
OF AN INTEGRATED CIRCUIT

Examiner:

Docket No.: 99-369/L13.12-0117

RECEIVED

FEB 22 2002

PRELIMINARY AMENDMENT

Technology Center 2100

Commissioner for Patents  
Washington, D.C. 20231

I HEREBY CERTIFY THAT THIS PAPER IS BEING  
SENT BY U.S. MAIL, FIRST CLASS, TO THE  
ASSISTANT COMMISSIONER FOR PATENTS,  
WASHINGTON, D.C. 20231, THIS

17 DAY OF January, 2002

*D. D. R.*  
PATENT ATTORNEY

Sir:

Prior to the first Official Action, it is respectfully  
requested that the following amendments be made.

IN THE SPECIFICATION

Please add the following paragraph beginning on page 1,  
line 5:

a1  
This application claims the benefits of U.S. Provisional  
Application No. 60/236,589, filed September 29, 2000, and entitled  
"METHOD AND APPARATUS FOR ADAPTIVE TIMING OPTIMIZATION OF AN  
INTEGRATED CIRCUIT DESIGN".

Please replace the paragraph beginning on page 1, line 5  
and ending on page 1, line 9 with the following:

a2  
Cross reference is made to U.S. Serial No. 09/964,011,  
filed on even date herewith, entitled "VIRTUAL TREE-BASED NETLIST  
MODEL AND METHOD OF DELAY ESTIMATION FOR AN INTEGRATED CIRCUIT  
DESIGN."